Abstracts

40 Gb/s analog IC chipset for optical receivers - AGC amplifier, full-wave rectifier and decision circuit implemented using self-aligned SiGe HBTs

K. Ohhata, F. Arakawa, T. Masuda, N. Shiramizu and K. Washio. "40 Gb/s analog IC chipset for optical receivers - AGC amplifier, full-wave rectifier and decision circuit implemented using self-aligned SiGe HBTs." 2001 MTT-S International Microwave Symposium Digest 01.3 (2001 Vol. III [MWSYM]): 1701-1704 vol.3.

40 Gb/s analog IC chipset, an AGC amplifier, a full-wave rectifier and a decision circuit, for optical receivers were developed using SiGe HBT technology. The high performance SiGe HBT and optimized circuit configuration make possible an AGC amplifier with a 47.8 GHz bandwidth, a full-wave rectifier, and a decision circuit with 40 Gb/s operation.

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